



*Handwritten initials: #15FW*

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q51885

Akitaka KIMURA

Allowed: July 7, 2004

Appln. No.: 09/161,981

Group Art Unit: 2812

Confirmation No.: 8269

Examiner: Savitri Mulpuri

Filed: September 29, 1998

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND METHOD  
FOR DEPOSITING SEMICONDUCTOR LAYER

**SUBMISSION OF REPLACEMENT DRAWINGS**

**MAIL STOP ISSUE FEE**

**ATTN: OFFICIAL DRAFTSPERSON**

Commissioner for Patents

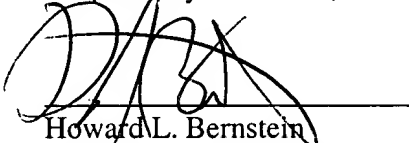
P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

As requested by the examiner, submitted herewith please find 4 sheets of replacement drawings in compliance with 37 C.F.R. § 1.84. The requested drawing change removes "prior art" from Figures 6-9. The Examiner is respectfully requested to acknowledge receipt of these drawings.

Respectfully submitted,

  
Howard L. Bernstein  
Registration No. 25,665

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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: February 1, 2005